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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Thomas J. CAMPANA, Jr., et al.

Serial No.:

07/702,938

Filed:

May 20, 1991

For:

SYSTEM FOR INTERCONNECTING ELECTRONIC MAIL SYSTEMS BY RF COMMUNICATIONS AND METHOD OF

OF OPERATION THEREOF

Group:

2608

Examiner:

G. Oehling

TRANSMITTAL OF FORMAL DRAWINGS

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

June 10, 1994

sir:

Enclosed are twelve (12) sheets of formal drawings, showing Figs. 1-12, in connection with the above-identified application.

Respectfully submitted,

Donald E. Stout

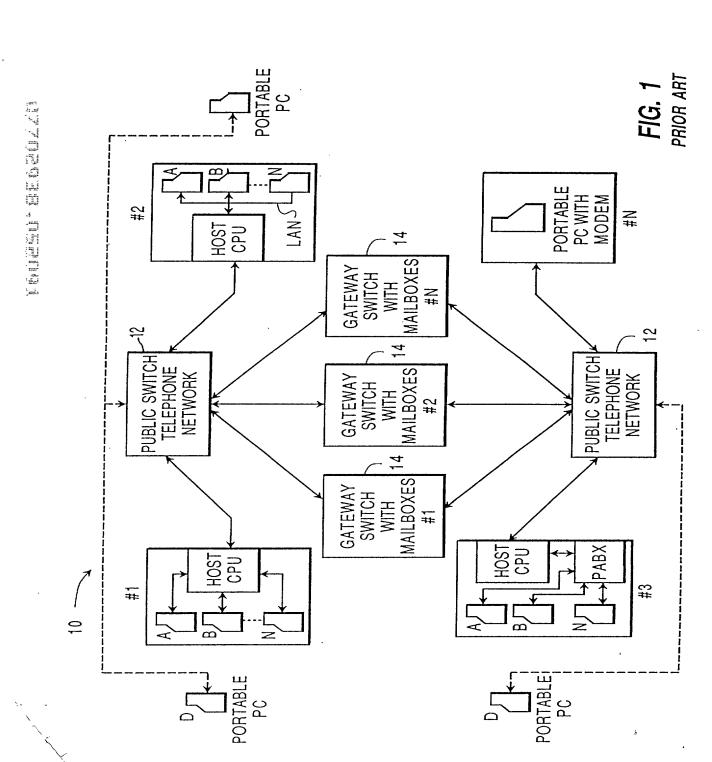
Registration no. 26,422

DES/bt (202) 828-0300 attachments

2 2 2 2

1.6. FIG. 8 1/08 (SUBSLASS 379 58

A Carlo



F1G. 2 (PRIOR ART) The first of the state of the s PERIPHERAL DEVICE HUB SWITCH #P TO OTHER HUBS PAGING RECEIVER(S) HUB SWITCH #2 TO CLOSEST HUBS 950XXXX 124 PAGING SERVICE LATA SWITCH #N <u>න</u> 126 TO CLOSEST LATA SWITCHES HUB SWITCH #1 TO <u></u> 950XXXX €, LATA SWITCH #1 127 TO CLOSEST HUBS 742 127 124C ≩

FIG. 3 (PRIOR ART) SWITCH

	LOCA	L SWITCH	MEMORY	MAP		
	fine with the first that the first t		156	15,8	160	
154					<i>,</i>	-
10.	SUBSCRIBER FILES N (9999)	FREQUEI FILES N	VCY (1,000)	LATA BUFFERS	LOCAL BUFFERS	
162 164 166	FILE # I (0,000) TELEPHONE #		I (1,000) FRE-	INBOUND	INBOUND PAGES	184
168	SUBSCRIBER AND PAGER ID CODE SERVICE OPTIONS	IN REGI	S USED ON COR-	LATA BUFFER	0	
	© NO SERVICE D LOCAL C REGIONAL	RESPON FILE #	DING TO	180		
					3	
170 <	① DATA SERVICE ② EXTERNAL DATA ⑤ SUBSCRIBER NAME/ACCOUNT			OUTBOUND	4	186
172 174 176 178	⑥ ACCOUNT # ⑦ PAGE COUNT (L,R,N)			LATA BUFFER	5	1 /
	<pre> # OF DATA CHARACTERS SENT DESTINATIONS AREA CODE(S) </pre>			182	6	1
			(000)		8	
	FILE # N (9,999)	FILE # N	(888)		9	
						ID CODE BUFFERS

SOUR FIG.

F1G. 4

		1 10.	~		
	fini finn finn f vő	(PRIOR A LATA SWITCH MI 190	RT) EMORY MAP 192	194	196
1000 1000 1000 1000 1000 1000 1000 100	HALLE STANK	190	, ,	/	
188	HUB BUFFERS	LOCAL BUFFERS	LATA ID MEMORY	OPTIONAL	OPTIONAL
198	OUTBOUND PAGES	INBOUND PAGES 202 OUTBOUND PAGES LOCAL # I	ALL PAGER ID CODES OF LOCAL#1	ALL CALL BUFFER PAGES FROM	ALL CALL BUFFER PAGES
200	INBOUND PAGES	204 OUTBOUND LOCAL # N (25)	ALL PAGER ID CODES OF LOCAL # N (26)	HUB SWITCH	LOCAL SWITCHES

.O. FIG.

FIG. 5

11 4.	(PRIOR ART)						
٧,	n de la companya de l	HUB SWITCH	MEMORY MAP				
1	206	208	210	212			
-	I HOD DOLLETO	LATA BUFFERS	LATA CODE TABLES N (100)	HUB ROUTING CODES N (1000) 224			
1000	INBOUND HUB# I	INBOUND LATA # I	LATA	ROUTING CODE 1,2,3,4,5,6 (312)			
	COLUMN TO THE CO	218	CODE 222 # 1				
	INBOUND	INBOUND LATA # N (100)					
	HUB # N (6) OUTBOUND HUB I	LATA # N (100) OUTBOUND LATA I					
-							
		220 ——					
			LATA CODE				
	OUTBOUND HUB # N (6)	OUTBOUND LATA # N (100)	# N (100)	ROUTING CODE # N (999)			

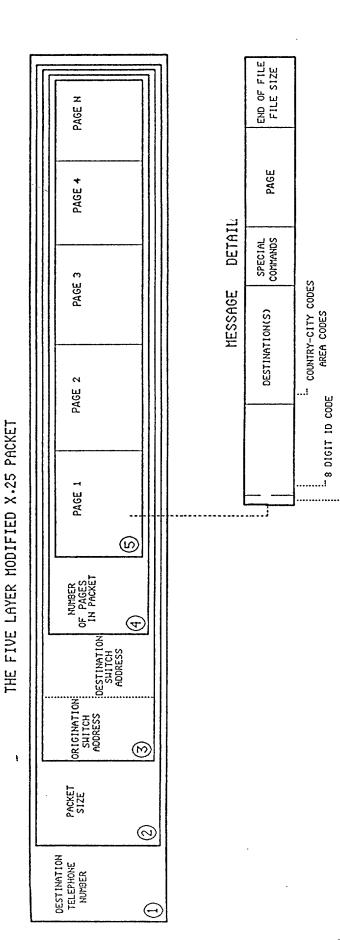
214

216

CLASS SURGLASS

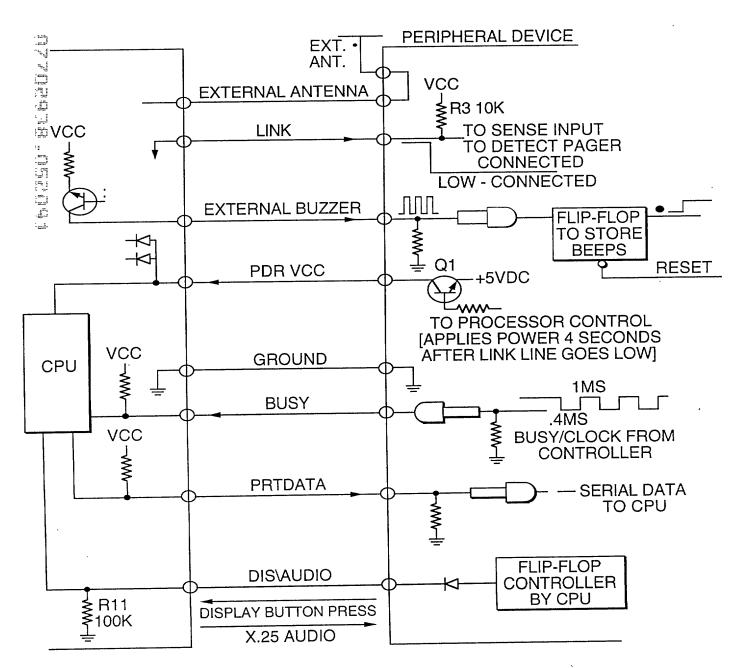
Collision and

F/G. 6 (Prior art)



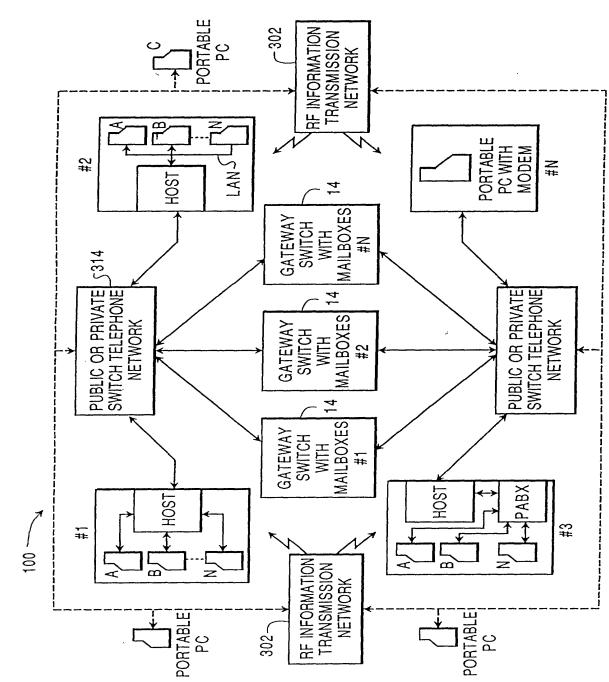
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FIG. 7 PRIOR ART

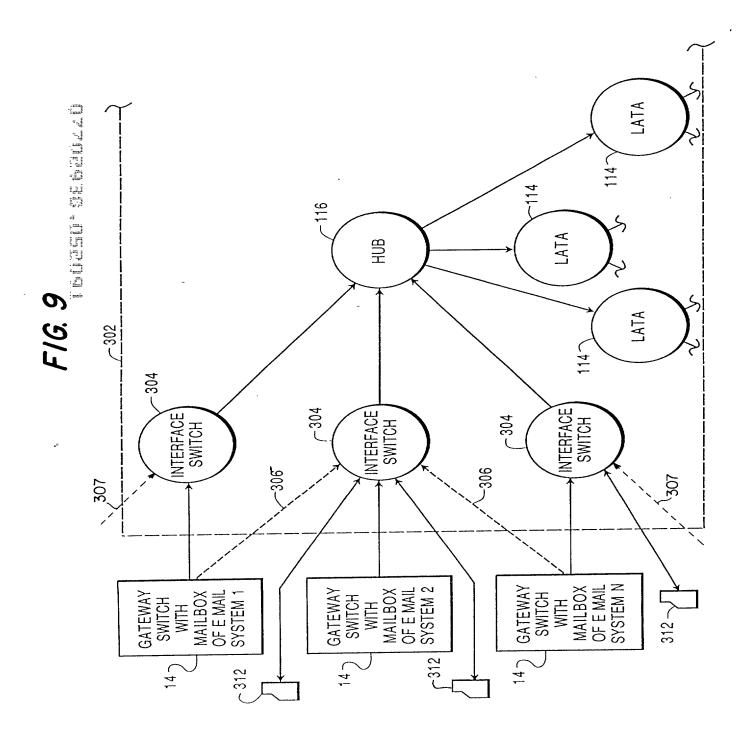


F16.8

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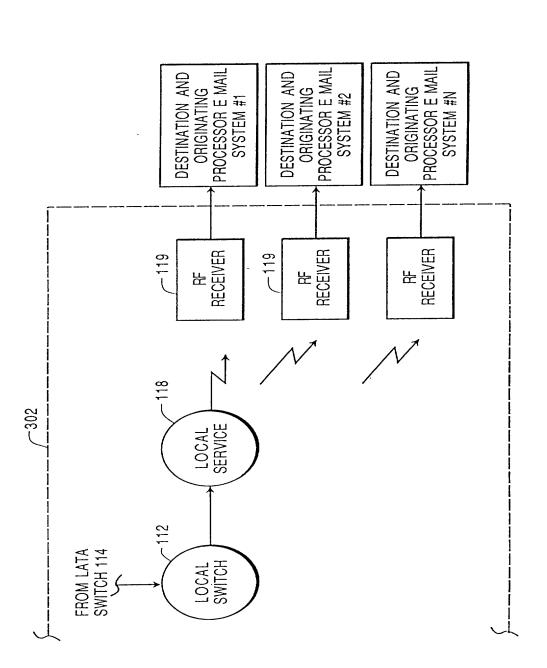
O.G. FIG.



O.G. FIG.

SAFTSTIAL.

F/6. 10



ULACS SUBCLASS

SWITCH 304	ADDS ID OF RF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION	ADDS ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION	NO ACTION OTHER THAN ID VERIFICATION	ADDS ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
GATEWAY SWITCH 14	NO-ACTION	NO-ACTION	ADDS WIRELESS DESTINATION	ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119	ADDS ID OF RECEIVER 119	NO-ACTION	NO-ACTION
ORIGINATING PROCESSOR	ADDS INTERFACE (WIRELESS) DESTINATION AND DESTINATION PROCESSOR	ADDS INTERFACE (WIRELESS) DESTINATION AND ID OF RECEIVER 119	ADDS DESTINATION PROCESSOR	ADDS DESTINATION PROCESSOR	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119(BY COMPARING DESTINATION PROCESSOR TO ID TABLE).
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